



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: **Mitsushi FUJIKI**

Art Unit: **2823**

Application Number: **10/772,253**

Examiner: **Thanh V. Pham**

Filed: **February 6, 2004**

Confirmation Number: **6491**

For: **METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE  
INCLUDING A LOW-TEMPERATURE LOWER ELECTRODE-  
FORMING STEP**

Attorney Docket Number: **042068**

Customer Number: **38834**

**AMENDMENT UNDER 37 C.F.R. § 1.116**  
**EXPEDITED PROCESSING REQUESTED**

**Mail Stop: AF**  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

August 3, 2007

Sir:

This paper is filed in response to the Office Action dated April 3, 2007.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 5 of this paper.